

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	990	automatic\$4 same resets\$4 same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:39
L2	4	USB same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) same (mute same (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 09:55
L3	2	USB same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 09:56
L4	2	USB same respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:00
L5	52	USB same (sc) or ((smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:40
L7	36	5 and reset respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:40
L8	1	5 and reset\$4 same (((non adj)2 respon\$6) near10 (mode or state)) or (mute near10 (mode or state)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:39
L9	7	(automatic\$4 or auto) same reset\$4 same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) same USB	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:23
L10	16	5 and 7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:40
L11	4	10 and status same register	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:55
L12	0	"6142796".ap.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 10:55

L13	0	(automatic\$4 or auto) same reset\$4 same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) same USB same abnormal\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:43
L14	2	(automatic\$4 or auto) same reset\$4 same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and USB same abnormal\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:29
L15	1	(automatic\$4 or auto) same reset\$4 and USB same abnormal\$3 and status same register	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:28
L16	0	(automatic\$4 or auto) same reset\$4 and USB same abnormal\$3 same status same register	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:28
L17	35	(automatic\$4 or auto) same reset\$4 and USB and abnormal\$3 and status same register	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:28
L18	1	17 and ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and USB same abnormal\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:30
L19	25	17 and ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:29
L20	10	19 and abnormal\$3 same (control\$3 or logic)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:30
L21	2	19 and abnormal\$3 same (control\$3 or logic) same reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:31
L22	13	19 and((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) same (control\$3 or logic) same reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:34
L23	68	((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and (status or register) same (abnormal or non adj2 respons\$3) same (control\$3 or logic) same reset\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:41

L24	87	23 and reset respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:04
L25	87	((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and (status or register) same (abnormal or non adj2 respons\$3) same (control\$3 or logic) same reset\$4 and reset respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:24
L26	1	((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card)) and (status or register) same (abnormal or non adj2 respons\$3) same (control\$3 or logic) same reset\$4 and reset same respond\$3 same command\$3 same (mute near10 (mode or state))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 11:42
L27	7	(automatic\$4 or auto) same resets\$4 same usb same ((sc) or (smart near3 card) or (IC near3 card) or (logic near2 circuit) or (chip near3 card))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:29
L28	3	27 and (respond\$3 or mute or abnormal\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:05
L29	0	27 same (abnormal or non adj2 respons\$3) same (check\$3 or determin\$4) same status	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:25
L30	0	27 same (abnormal or non adj2 respons\$3) same (check\$3 or determin\$4) same status same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:25
L31	0	27 and (abnormal or non adj2 respons\$3) same (check\$3 or determin\$4) same status same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:26
L32	0	27 and (check\$3 or determin\$4) same status same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:27
L33	0	27 and (check\$3 or determin\$4) same (status or abnormal) same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:27

L35	2	25 and (check\$3 or determin\$4) same (status or abnormal) same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:29
L36	2	(automatic\$4 or auto) same reset\$4 same usb and ((sc) or (smart near3 card) or (IC near3 card) or (chip near3 card)) and (check\$3 or determin\$4) same (status or abnormal) same rest\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/03/03 12:30